

PRIOR ART

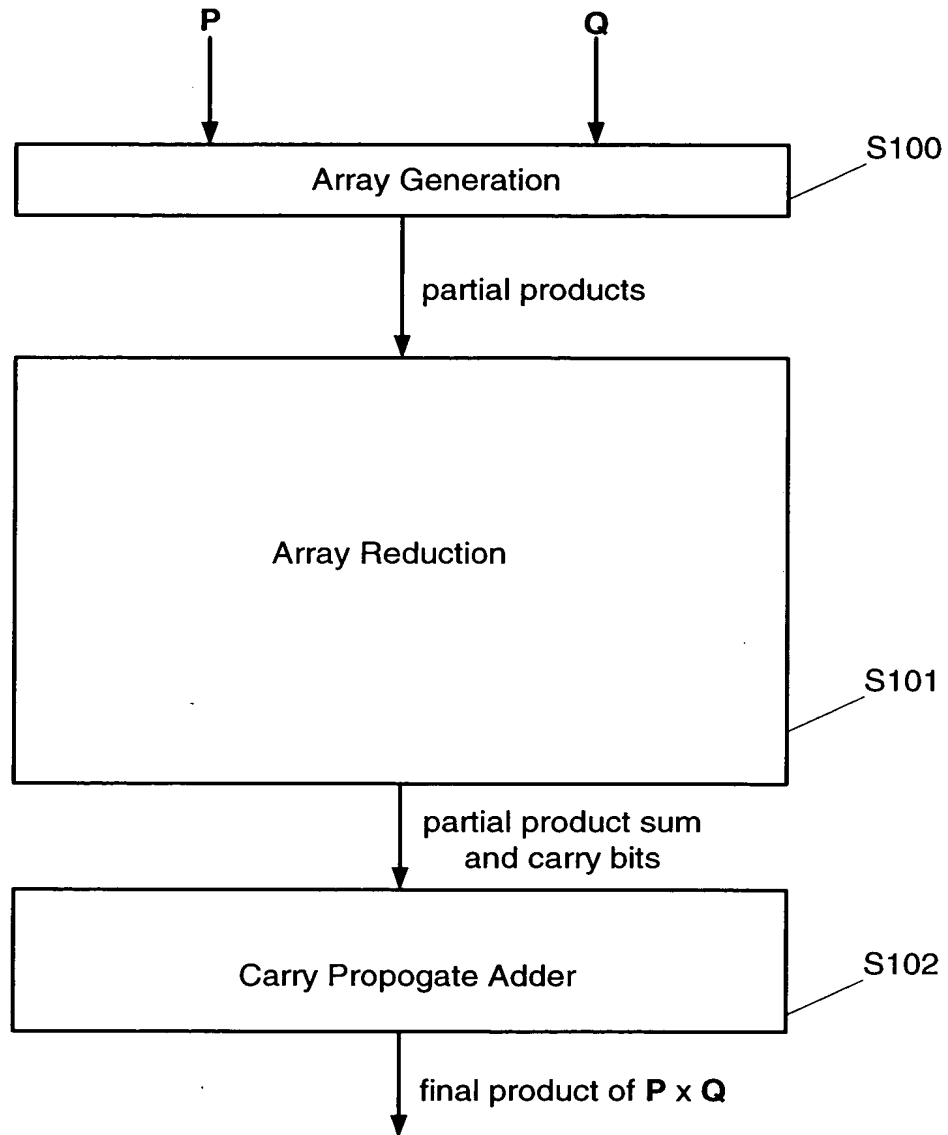


Figure 1

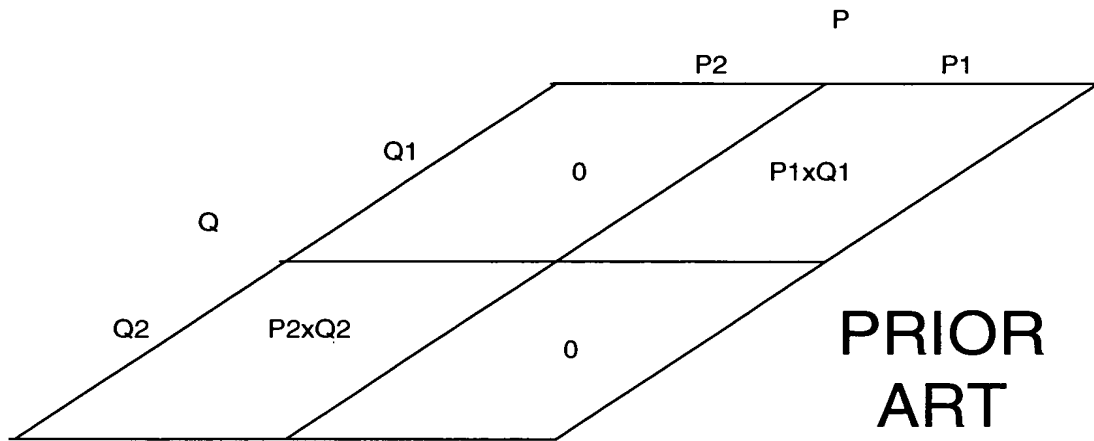


Figure 2

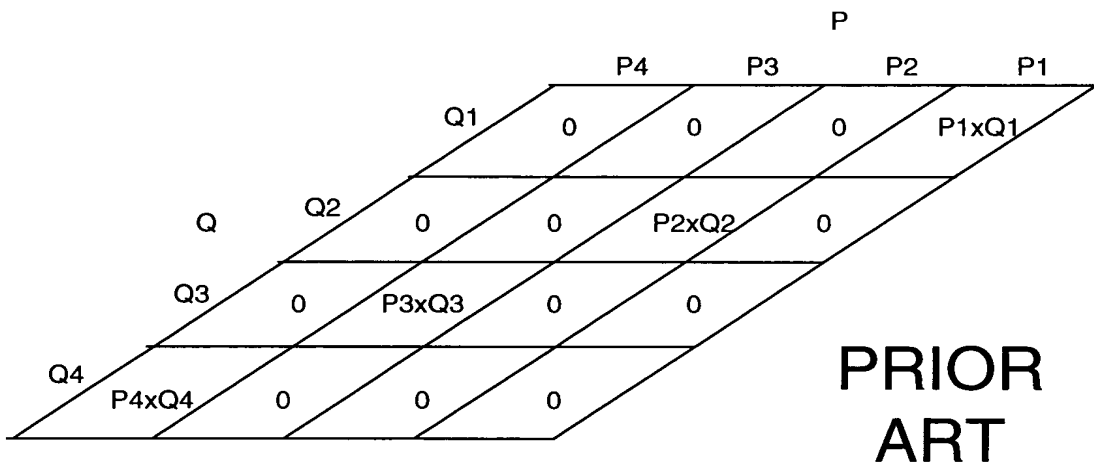
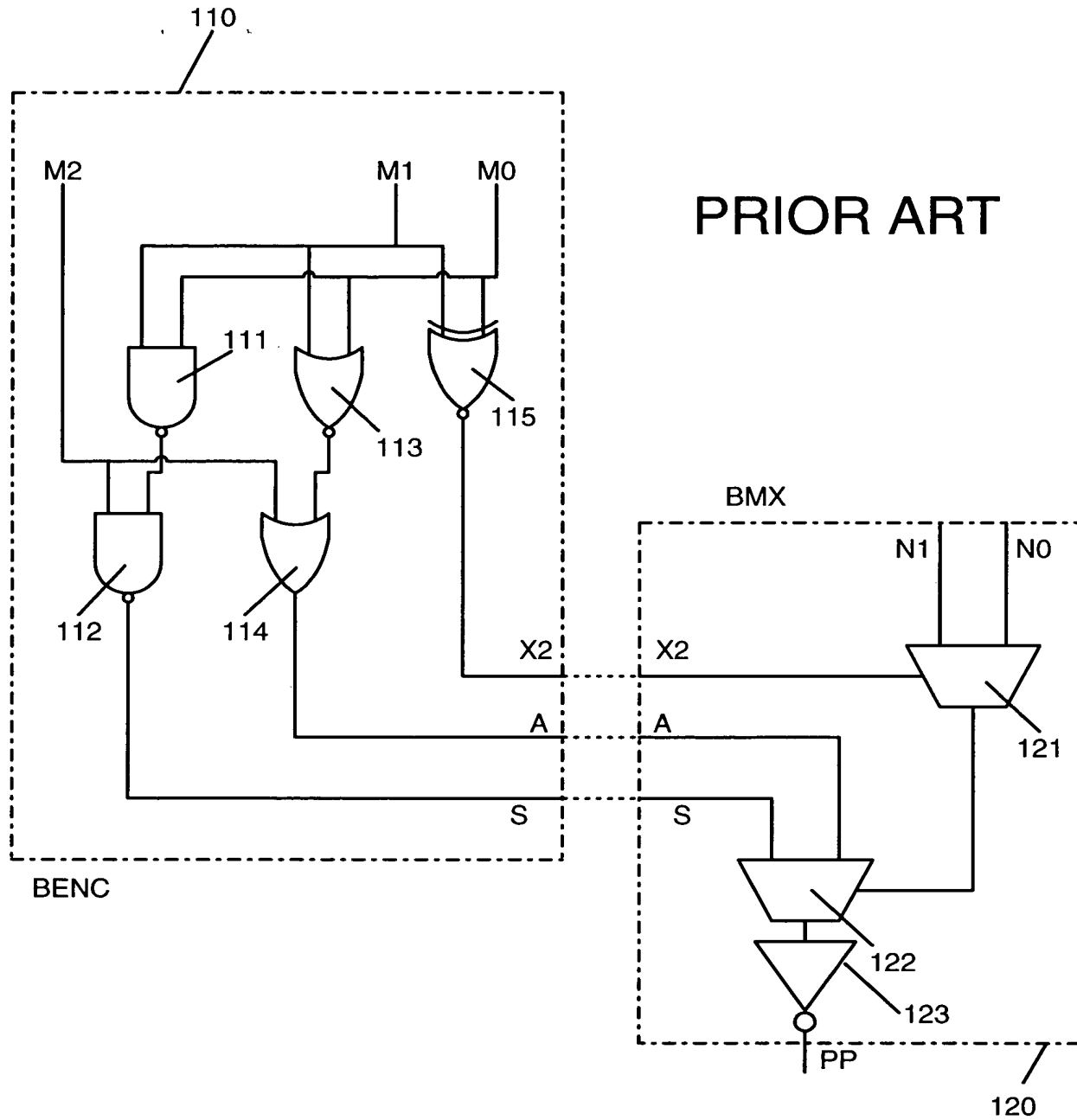


Figure 3

**Figure 4**

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M2	M1	M0	Action	A	S	X2	BMX output
0	0	0	multiply by 0	1	1	1	0
0	0	1	multiply by 1	0	1	0	N1
0	1	0	multiply by 1	0	1	0	N1
0	1	1	multiply by 2	0	1	1	N0
1	0	0	multiply by -2	1	0	1	¬N0
1	0	1	multiply by -1	1	0	0	¬N1
1	1	0	multiply by -1	1	0	0	¬N1
1	1	1	multiply by 0	1	1	1	0

Figure 5

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Redundant form of $3 \times N$ (16 bit):

N • • • • • • • • • • • • • • • •
 $2N$ • • • • • • • • • • • • • • • •

4-bit adders to be applied:

N

•	•	•	•
•	•	•	•

•	•	•	•
•	•	•	•

•	•	•	•
•	•	•	•

•	•	•	•
•	•	•	•

 •
 $2N$ • • • • • • • • • • • • • • • •

the result representing $3 \times N$:

$3N$ • • • • • • • • • • • • • • • •
 carries • • • •

reducing $\text{Bias} + 3 \times N$ ($\text{Bias} = 8736_{\text{dec}} = 10001000100000_{\text{bin}}$)

$3N$ • • • • • • • • • • • • • • • •
 carries • • • • • • • • • • • •

•
•
1

 0 0 0

•
•
1

 0 0 0

•
•
1

 0 0 0 0 0 0
 ↙ ↘ ↙ ↘ ↙ ↘

Biased partially redundant form of a number ($\text{Bias} + X$)

• • • • • • • • • • • • • • • •
 • • •

The inverse ($\text{Bias} - X = -(\text{Bias} + X) + 2 \times \text{Bias}$) can be represented in the form

• • • • • • • • • • • • • • • •
 1 1 1 • 1 1 1 • 1 1 1 • 1 1 • • • • • •
 $2 \times \text{Bias}$ 1 0 0 0 1 0 0 0 1 0 0 0 0 0 0 0 0 0 1

 • • • • • • • • • • • • • • • •
 • • • 1

Figure 6

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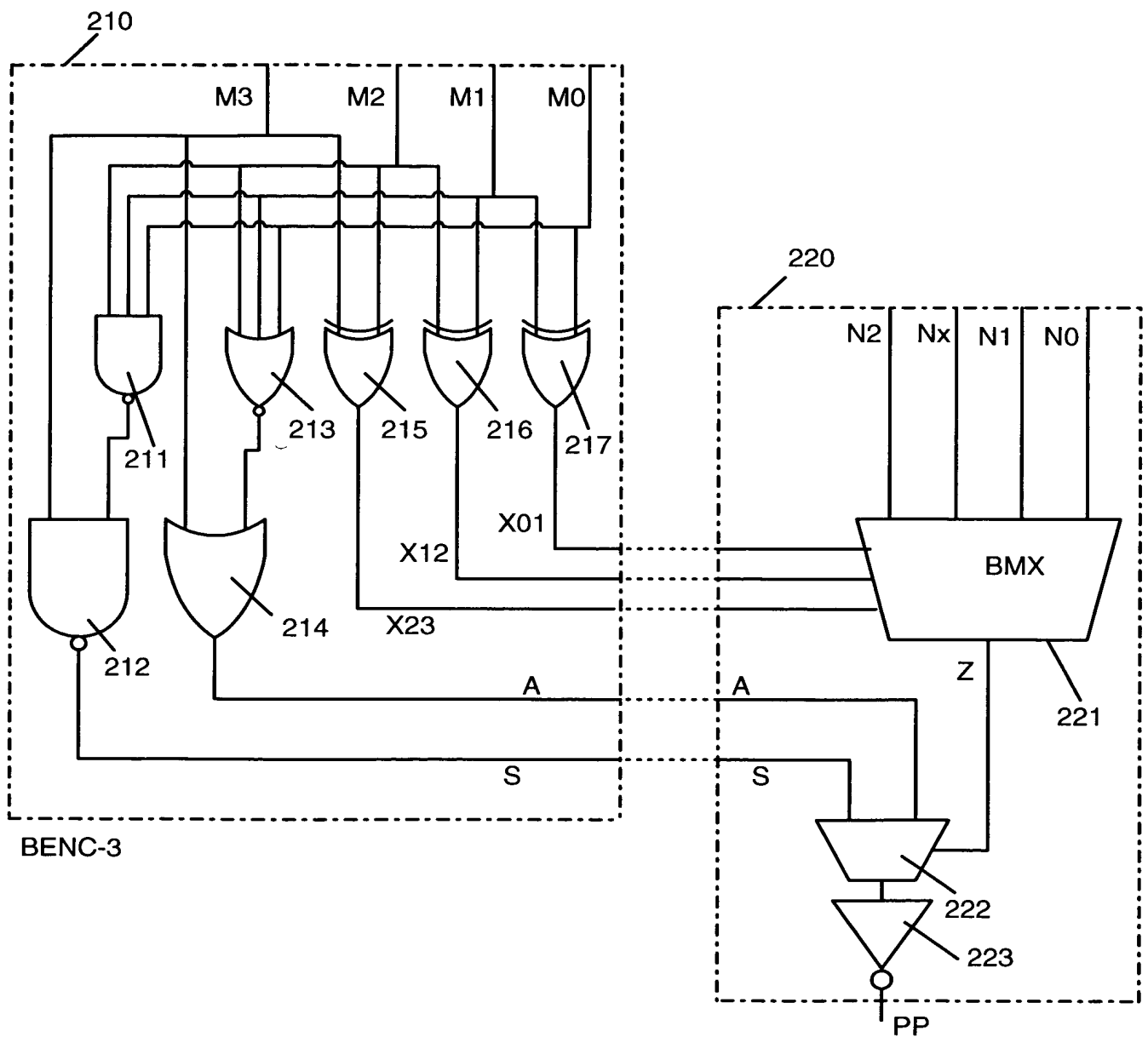


Figure 7

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M3	M2	M1	M0	Action	BMX output	A	S	X01	X12	X23
0	0	0	0	multiply by 0	0	1	1	0	0	0
0	0	0	1	multiply by 1	N2	0	1	1	0	0
0	0	1	0	multiply by 1	N2	0	1	1	1	0
0	0	1	1	multiply by 2	N1	0	1	0	1	0
0	1	0	0	multiply by 2	N1	0	1	0	1	1
0	1	0	1	multiply by 3	Nx	0	1	1	1	1
0	1	1	0	multiply by 3	Nx	0	1	1	0	1
0	1	1	1	multiply by 4	N0	0	1	0	0	1
1	0	0	0	multiply by -4	\neg N0	1	0	0	0	1
1	0	0	1	multiply by -3	\neg Nx	1	0	1	0	1
1	0	1	0	multiply by -3	\neg Nx	1	0	1	1	1
1	0	1	1	multiply by -2	\neg N1	1	0	0	1	1
1	1	0	0	multiply by -2	\neg N1	1	0	0	1	0
1	1	0	1	multiply by -1	\neg N2	1	0	1	1	0
1	1	1	0	multiply by -1	\neg N2	1	0	1	0	0
1	1	1	1	multiply by 0	0	1	1	0	0	0

Figure 8

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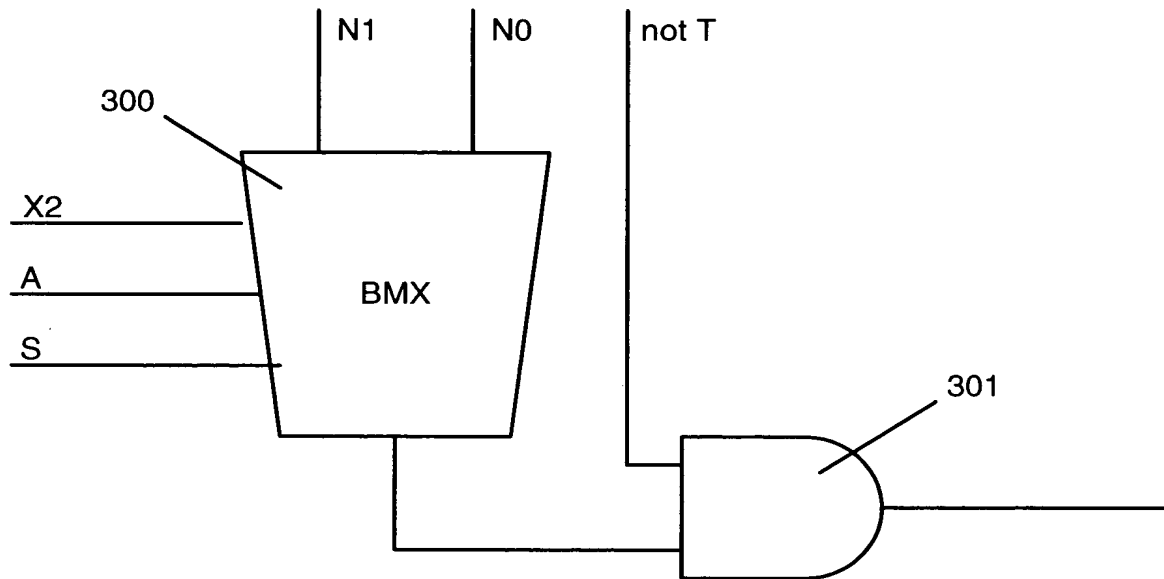


Figure 9a

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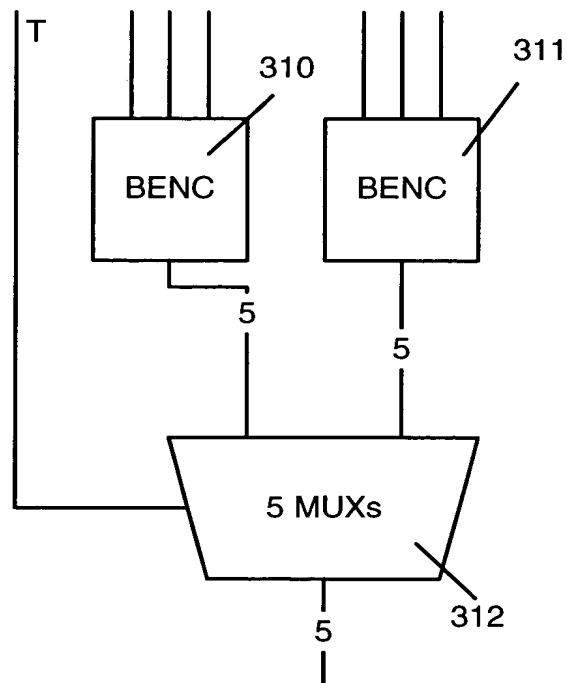
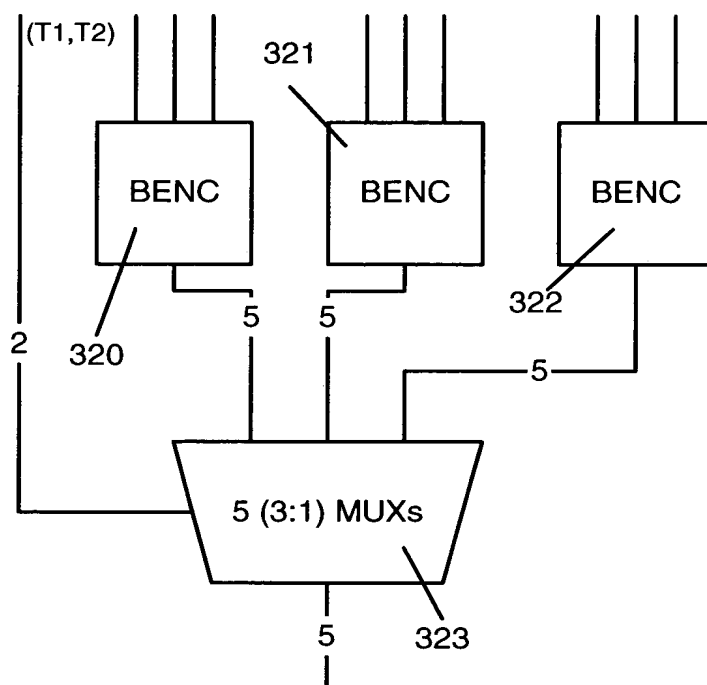


Figure 9b

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**Figure 9c**

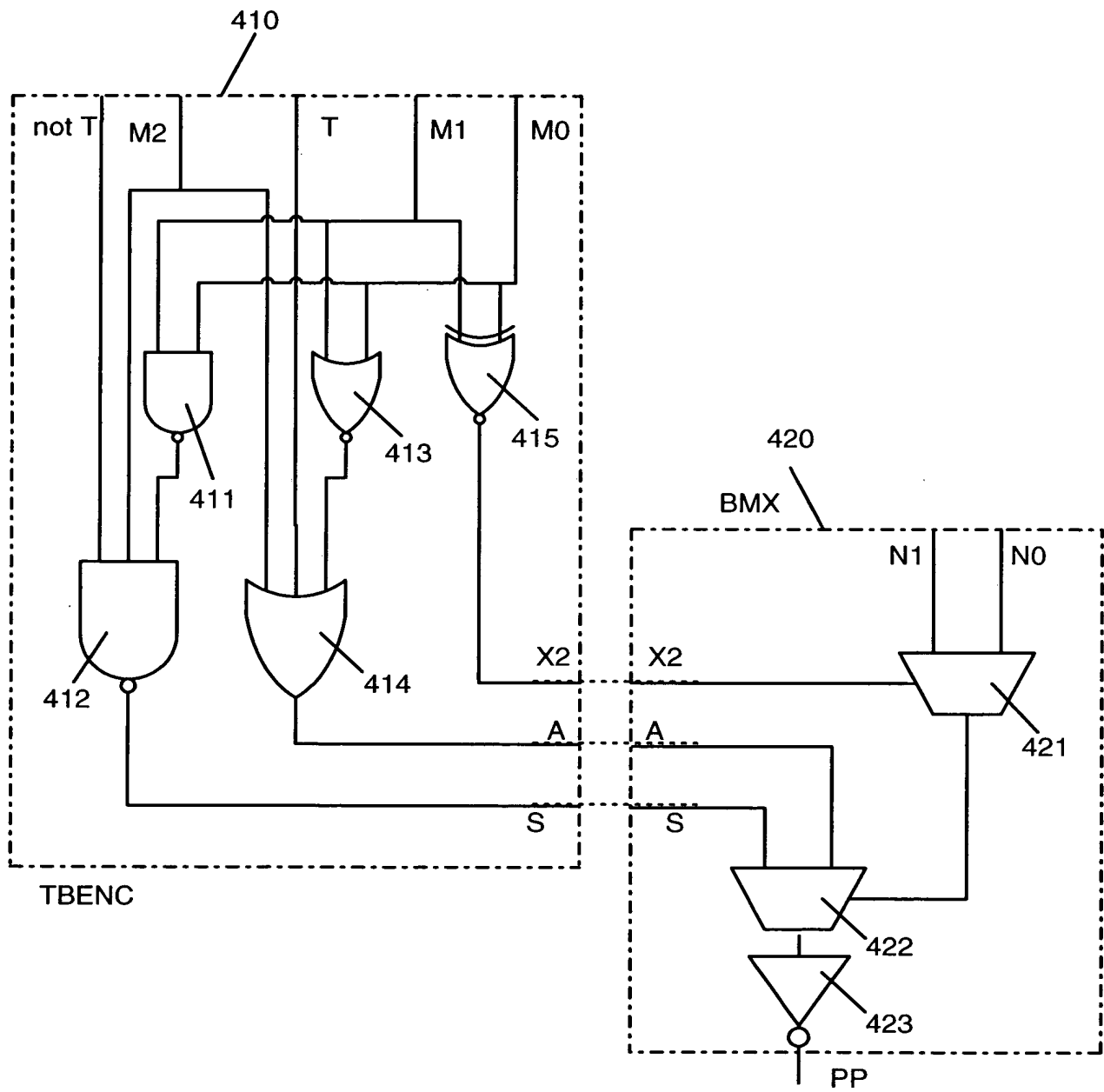
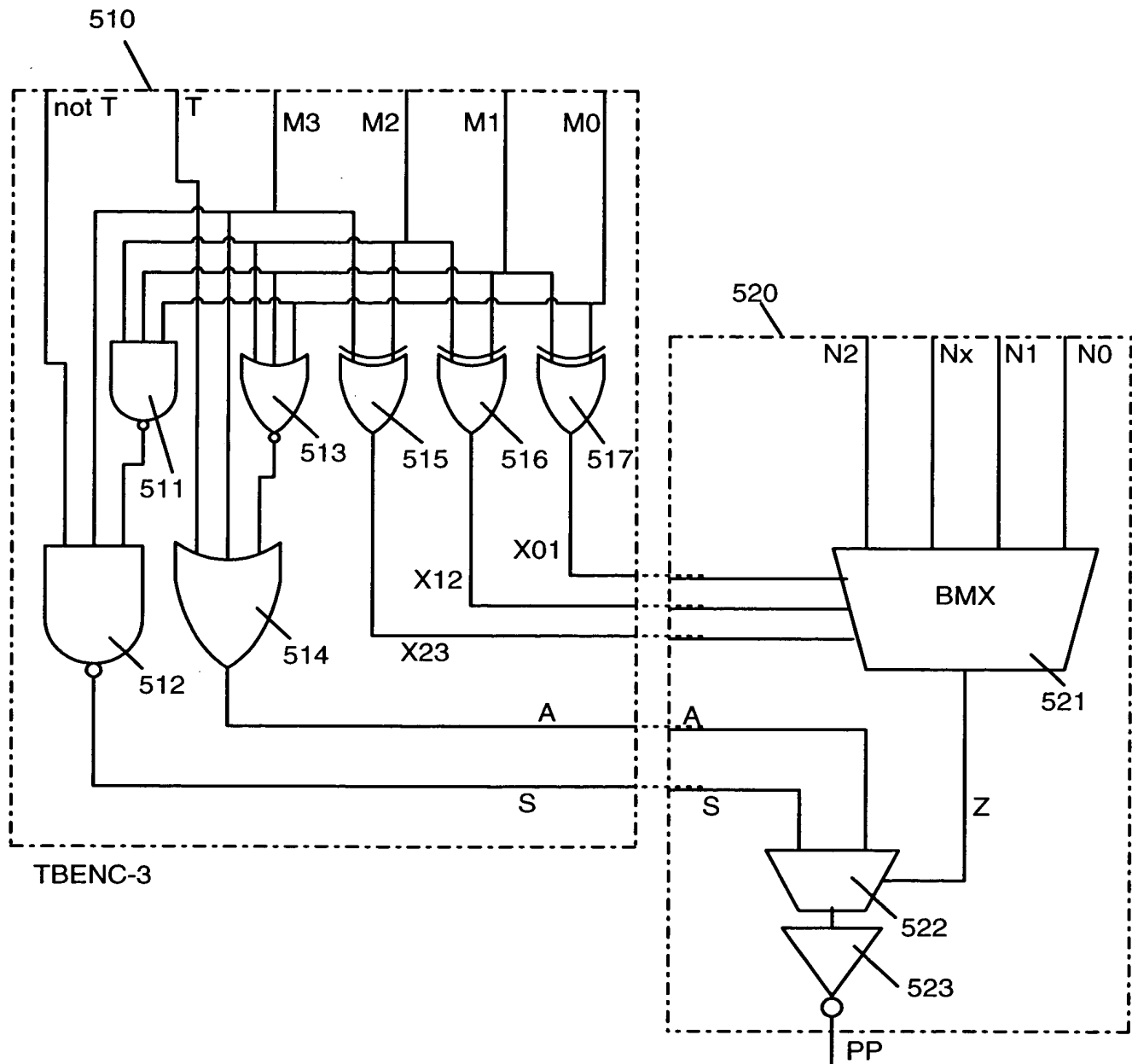


Figure 10

**Figure 11**

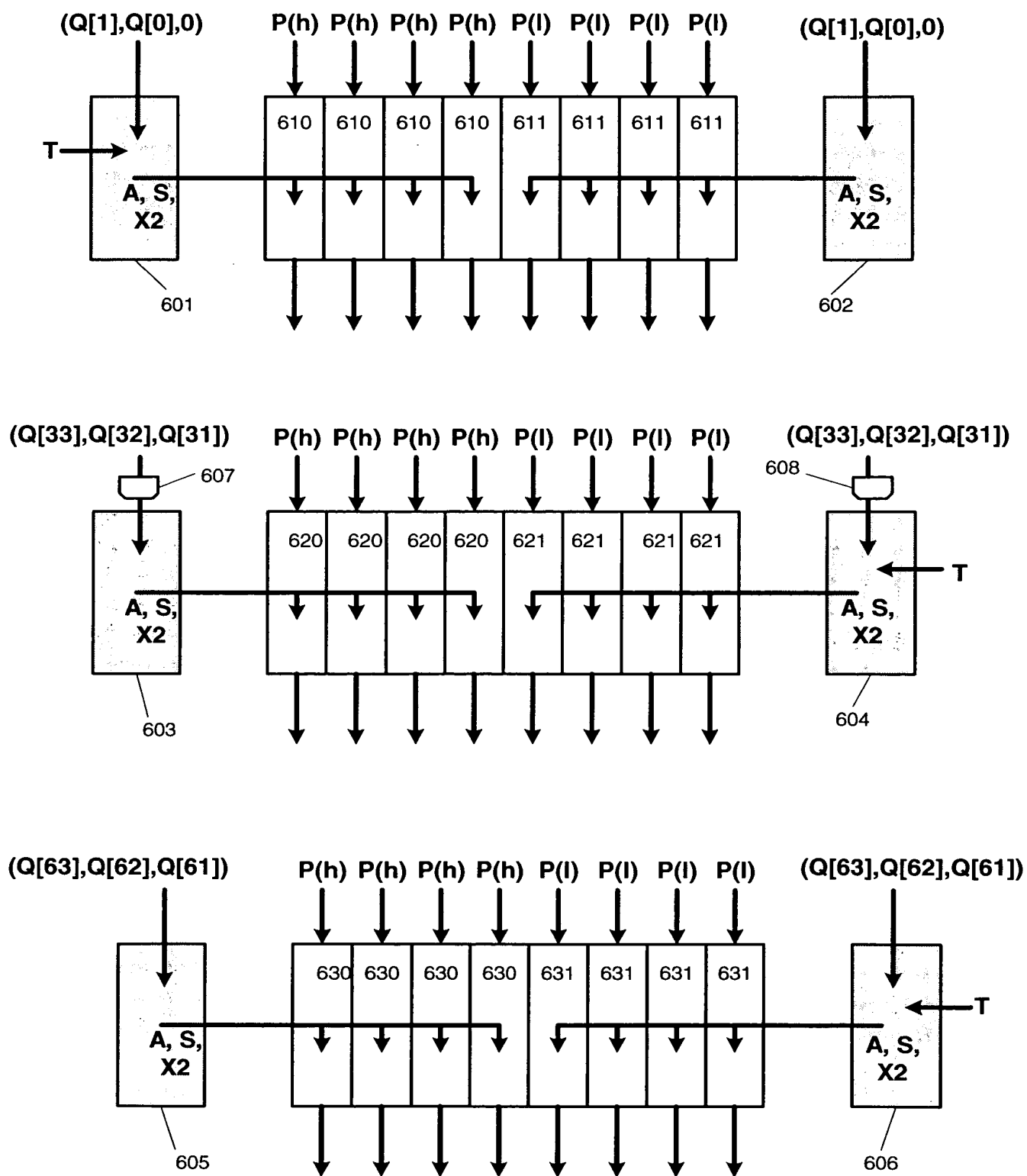


Figure 12